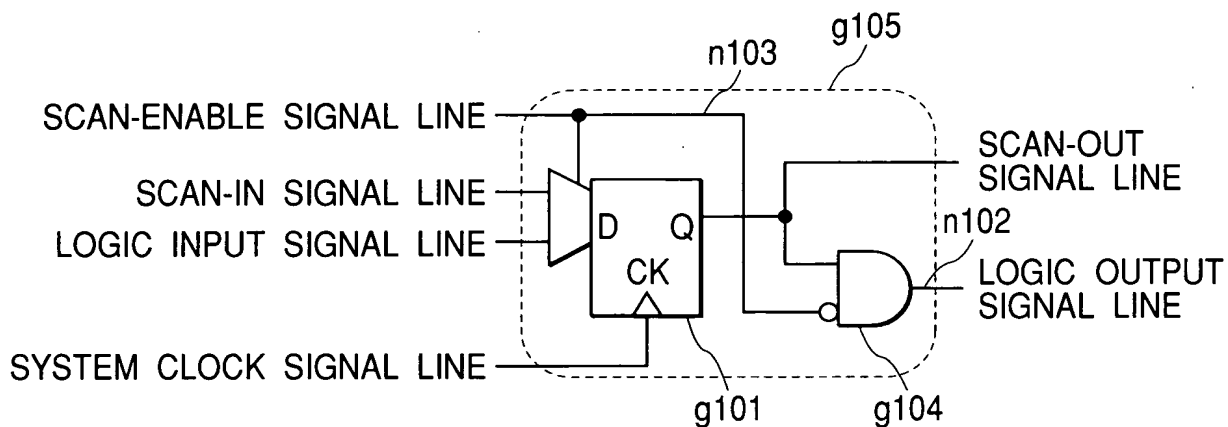
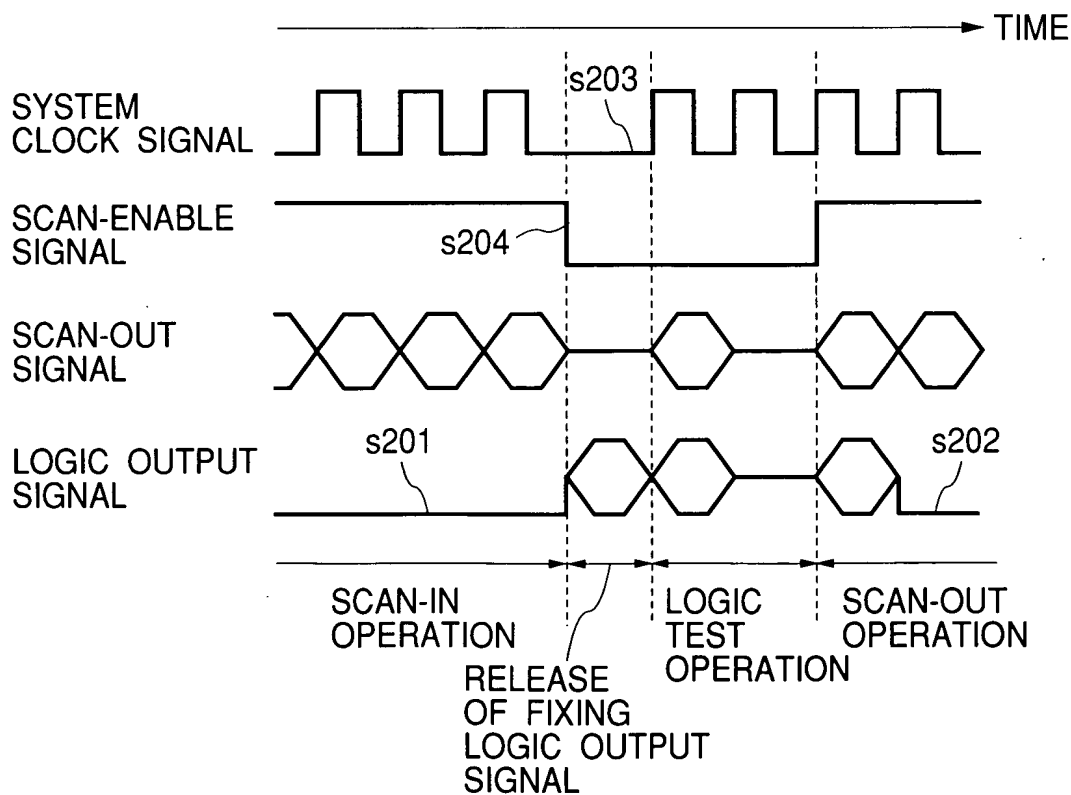


1 / 20

**FIG. 1****FIG. 2**

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FIG. 3

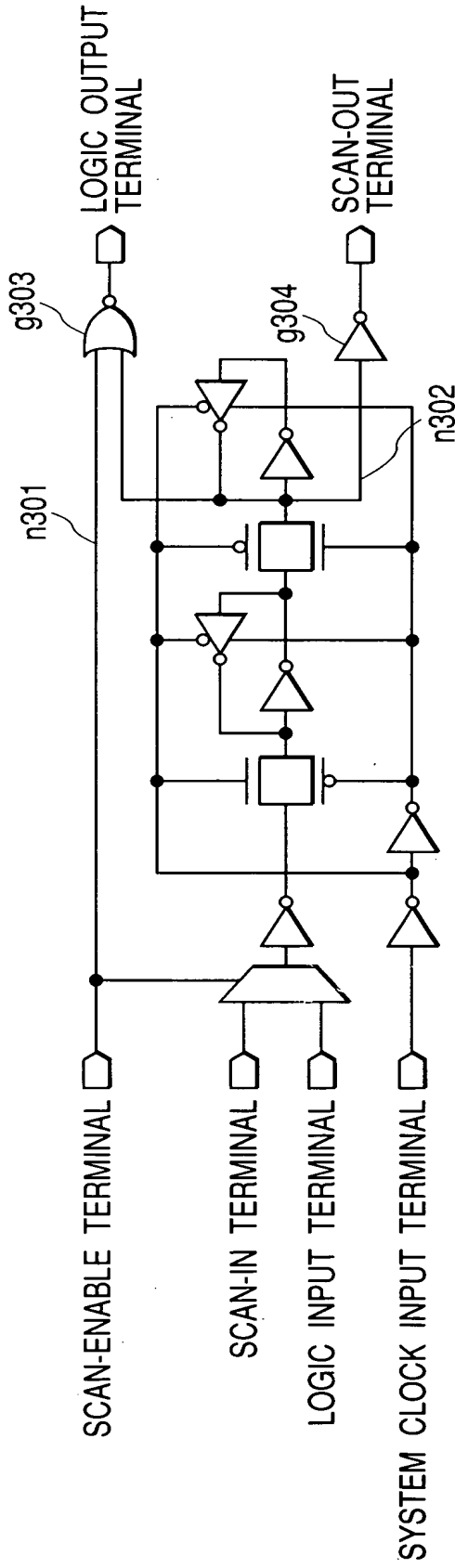
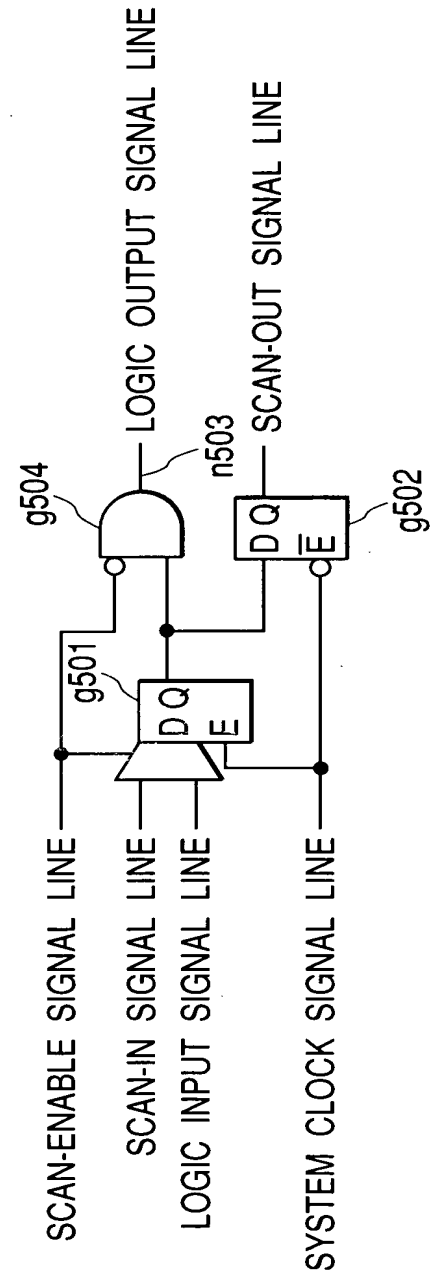
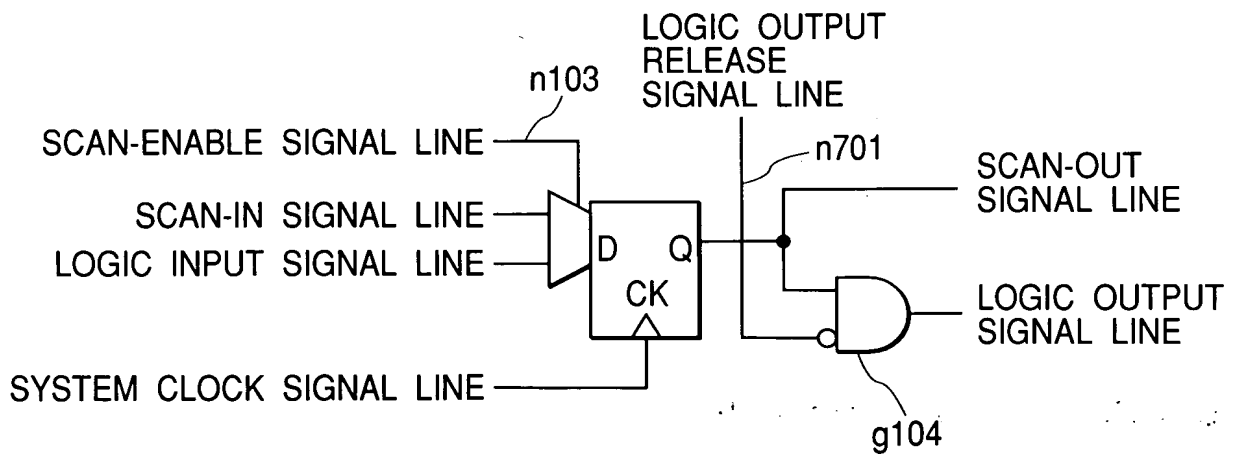


FIG. 4



**FIG. 6**



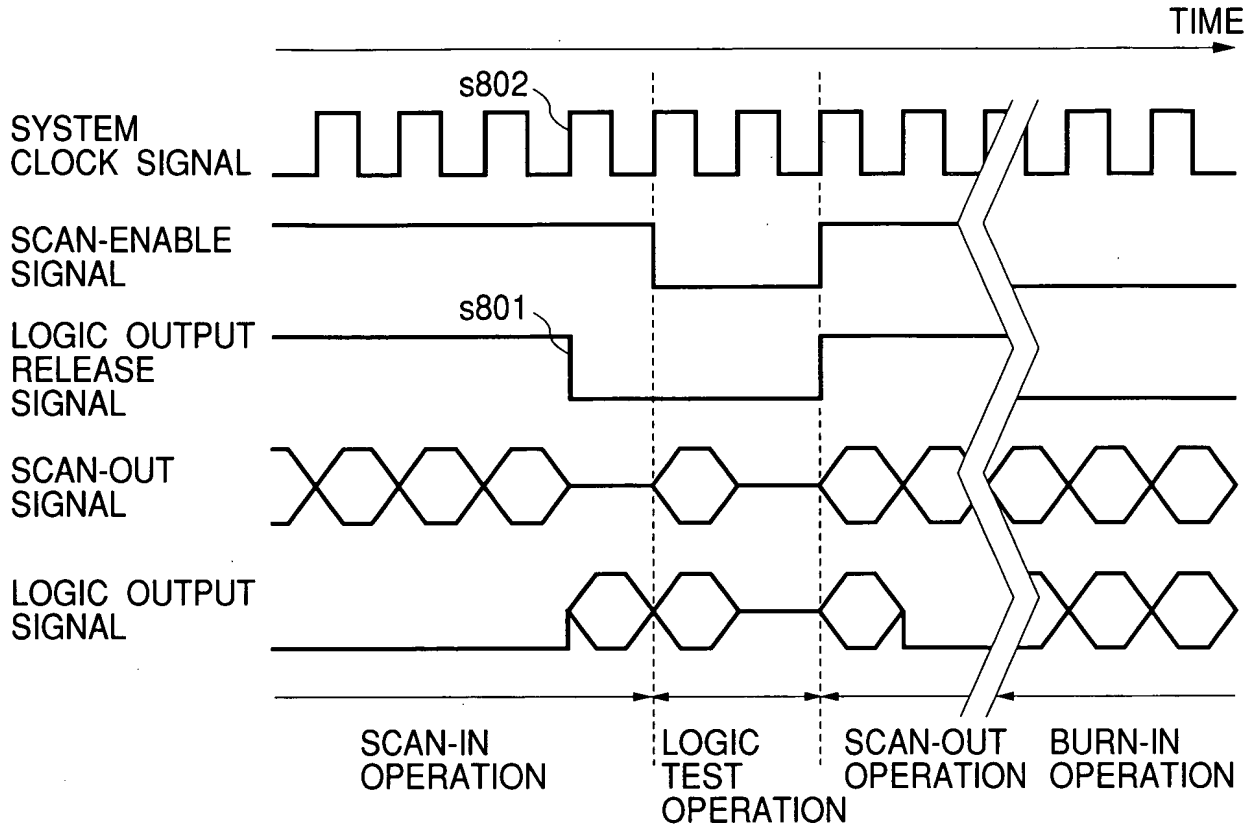
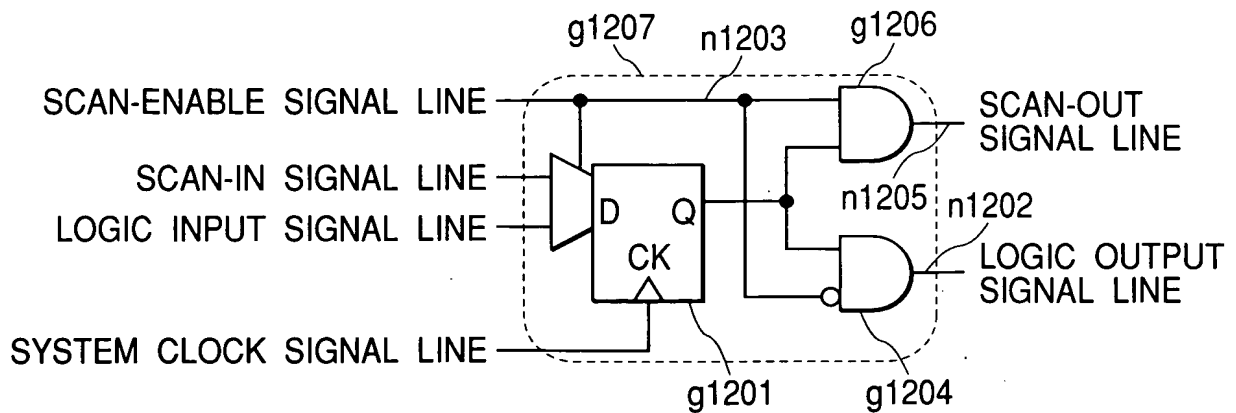
**FIG. 7****FIG. 8**



FIG. 10

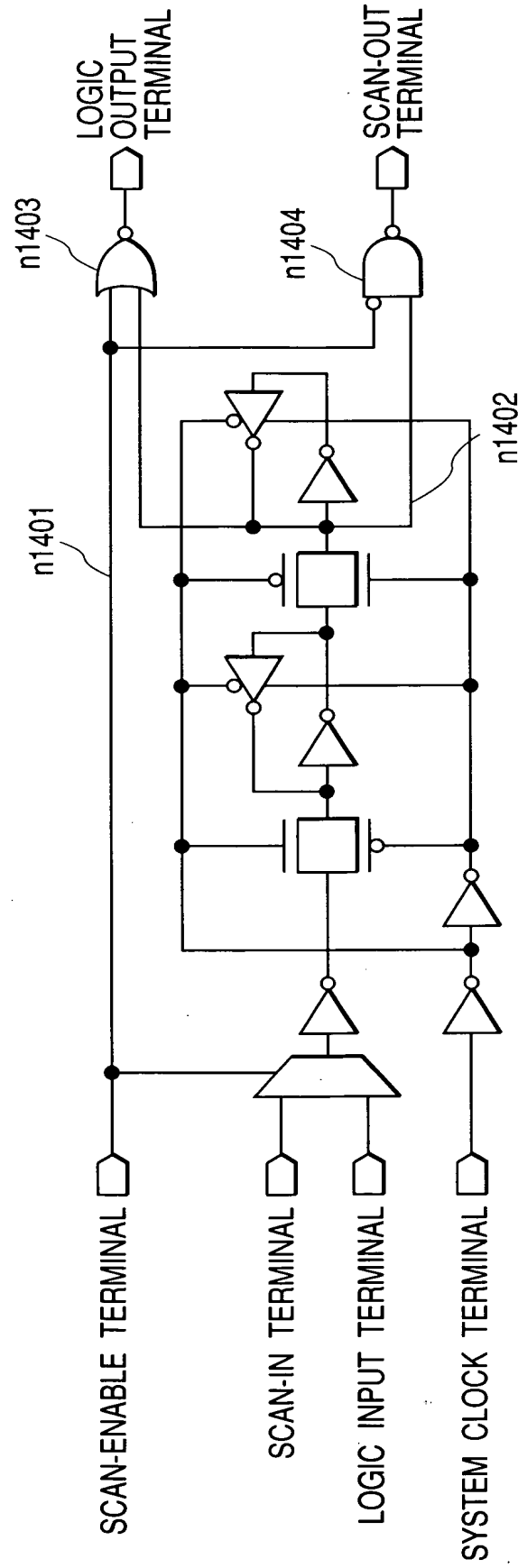
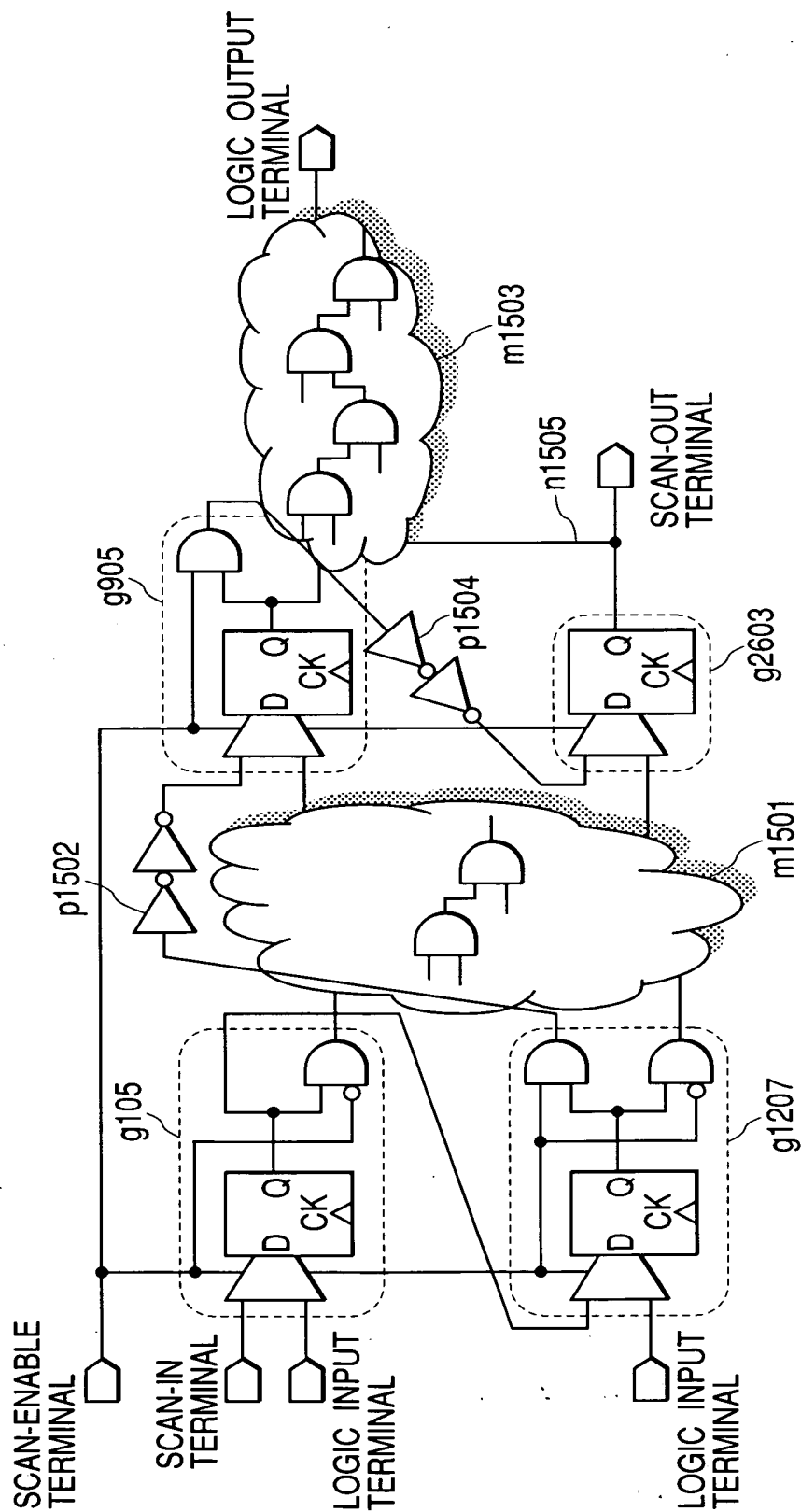


FIG. 11



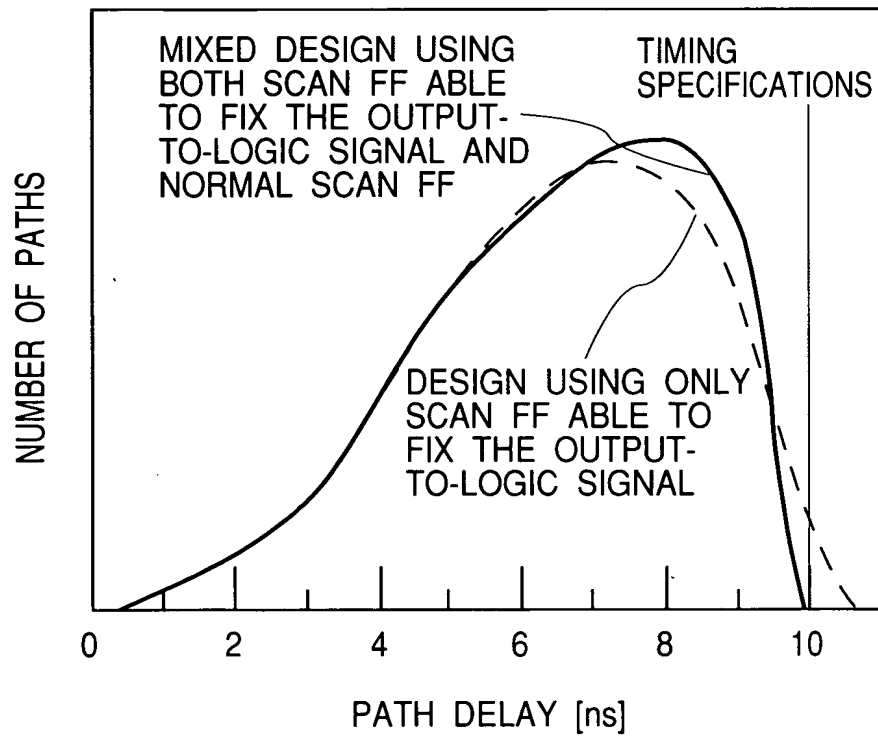
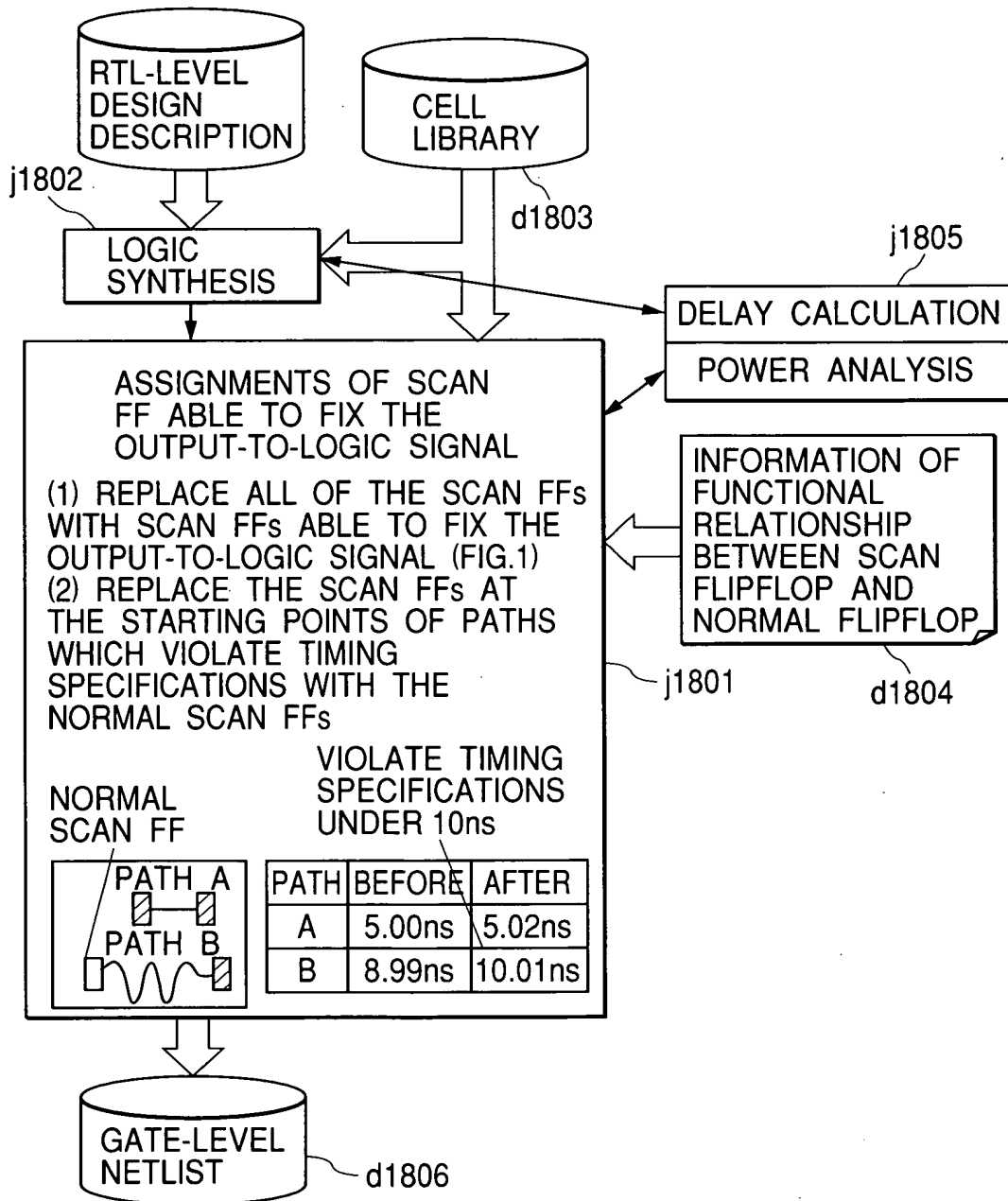
*FIG. 12*



FIG. 13



**FIG. 14**

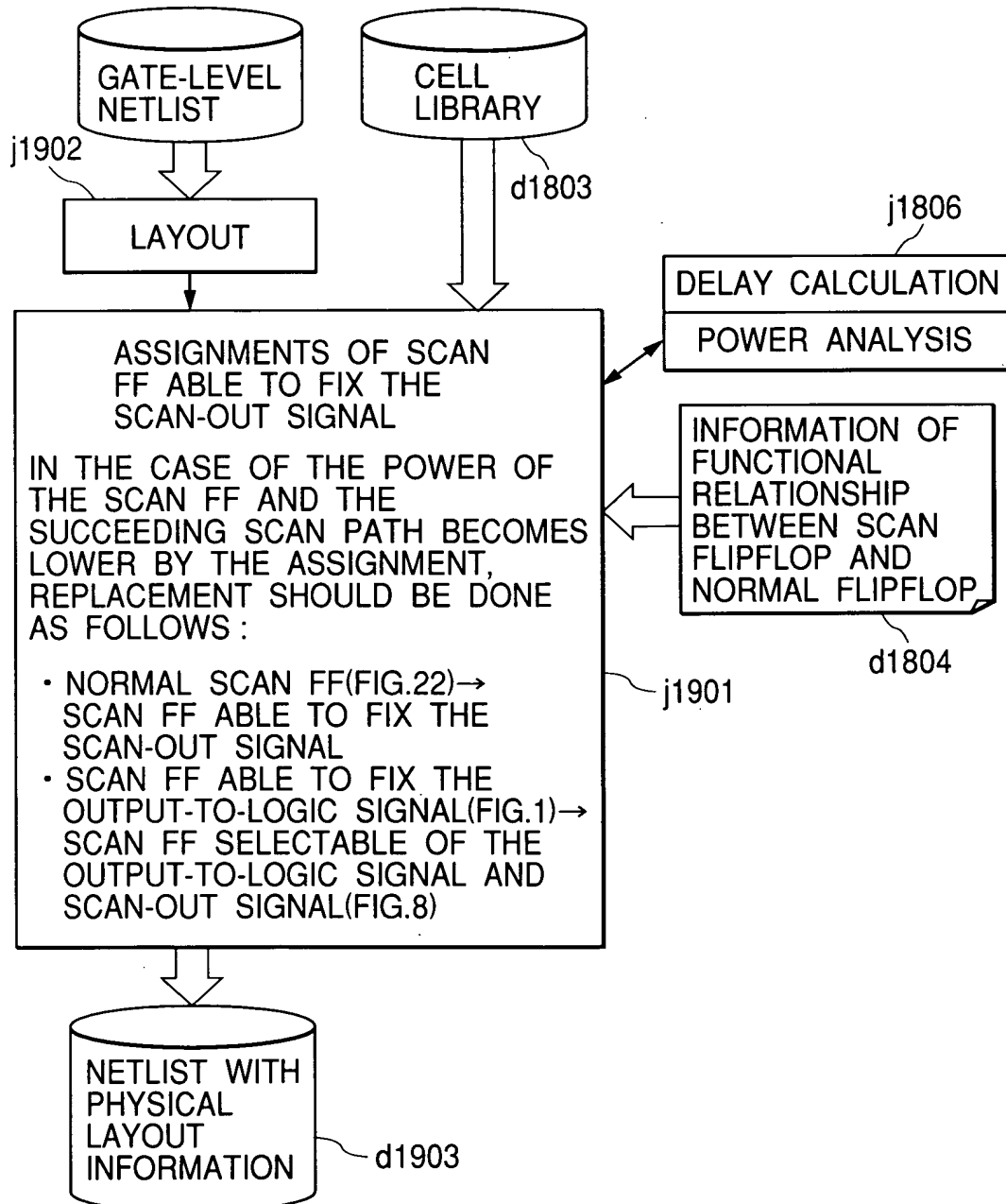


FIG. 15

PINS OF scandff1							PINS OF scandff1						
	PINS OF scandff1							PINS OF scandff1					
	d	si	se	cp	q			d	si	se	cp	q	
PINS OF logicmasked_scandff1	d	V					PINS OF Logicscanswitched_scandff1	d	V				
	si		V					si		V			
	se			V				se			V		
	cp				V			cp				V	
	q					V		q					V
	so					V		so					V

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FIG. 16

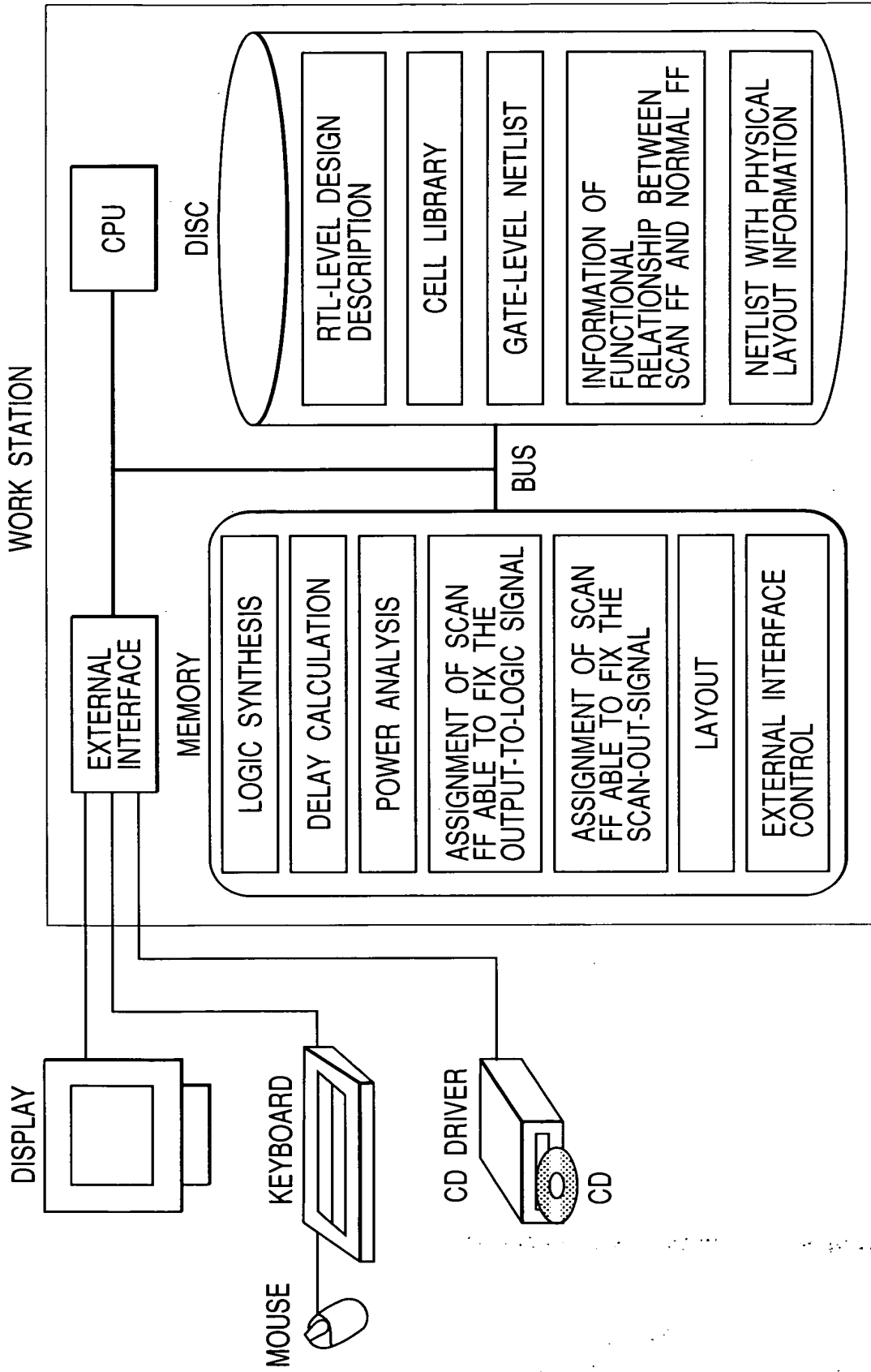


FIG. 17

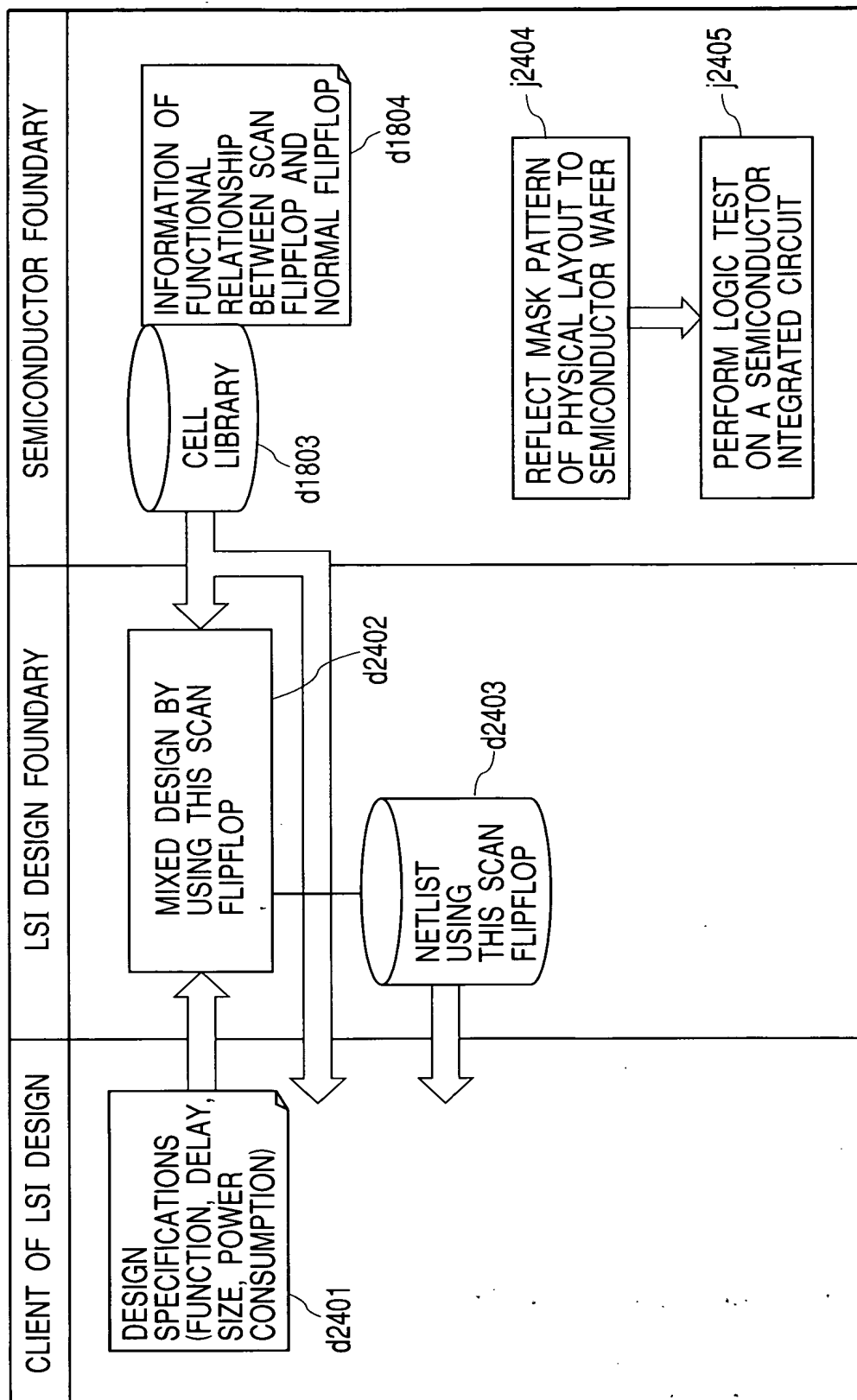


FIG. 18

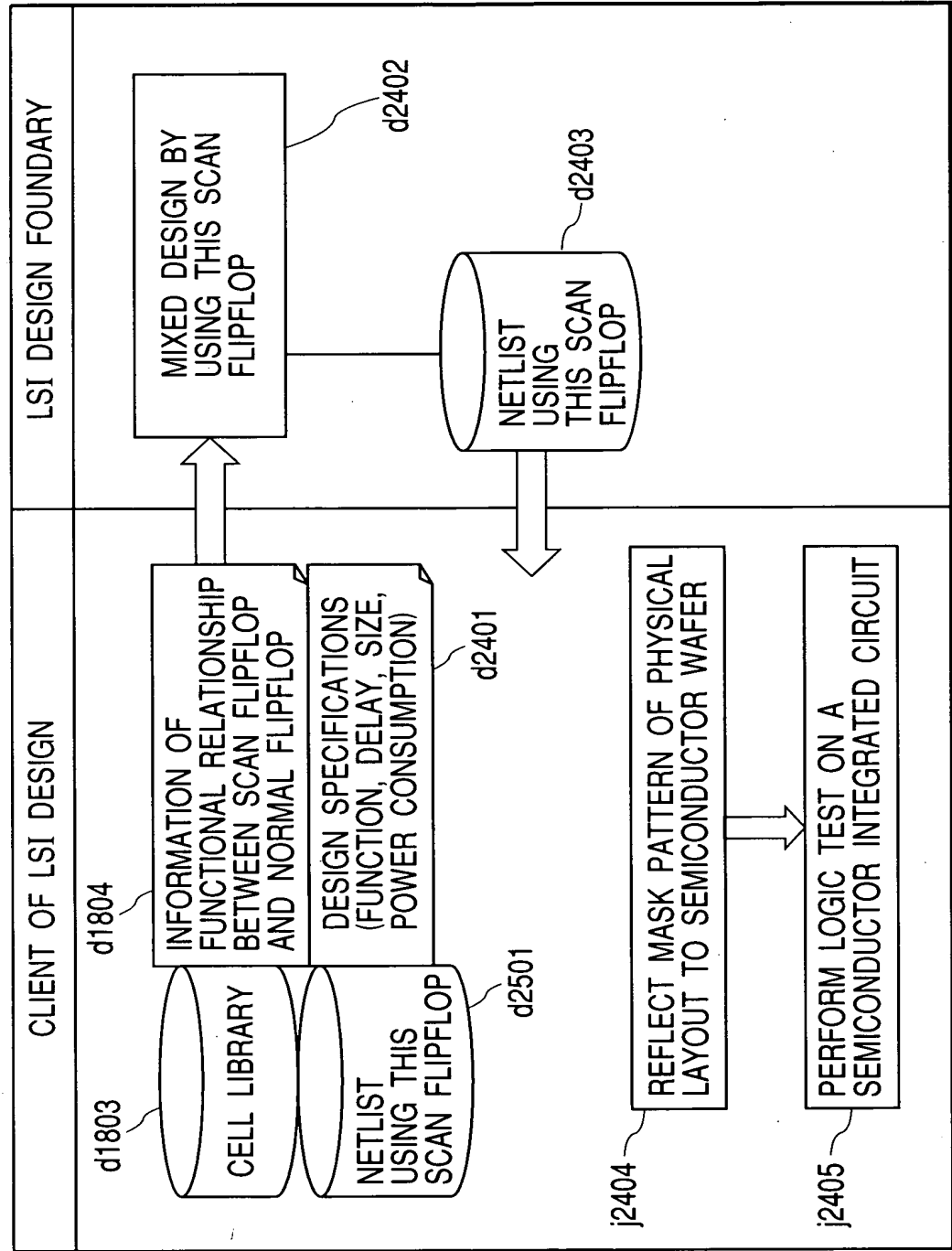


FIG. 19

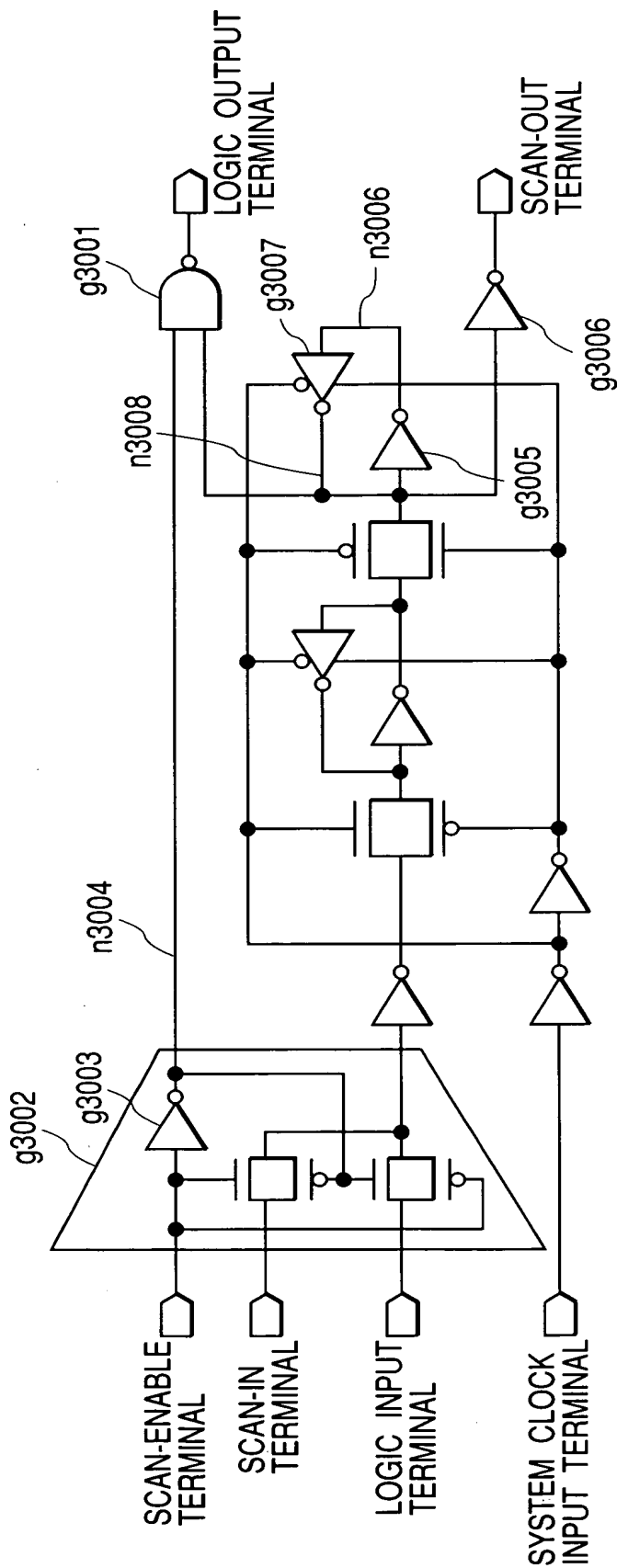
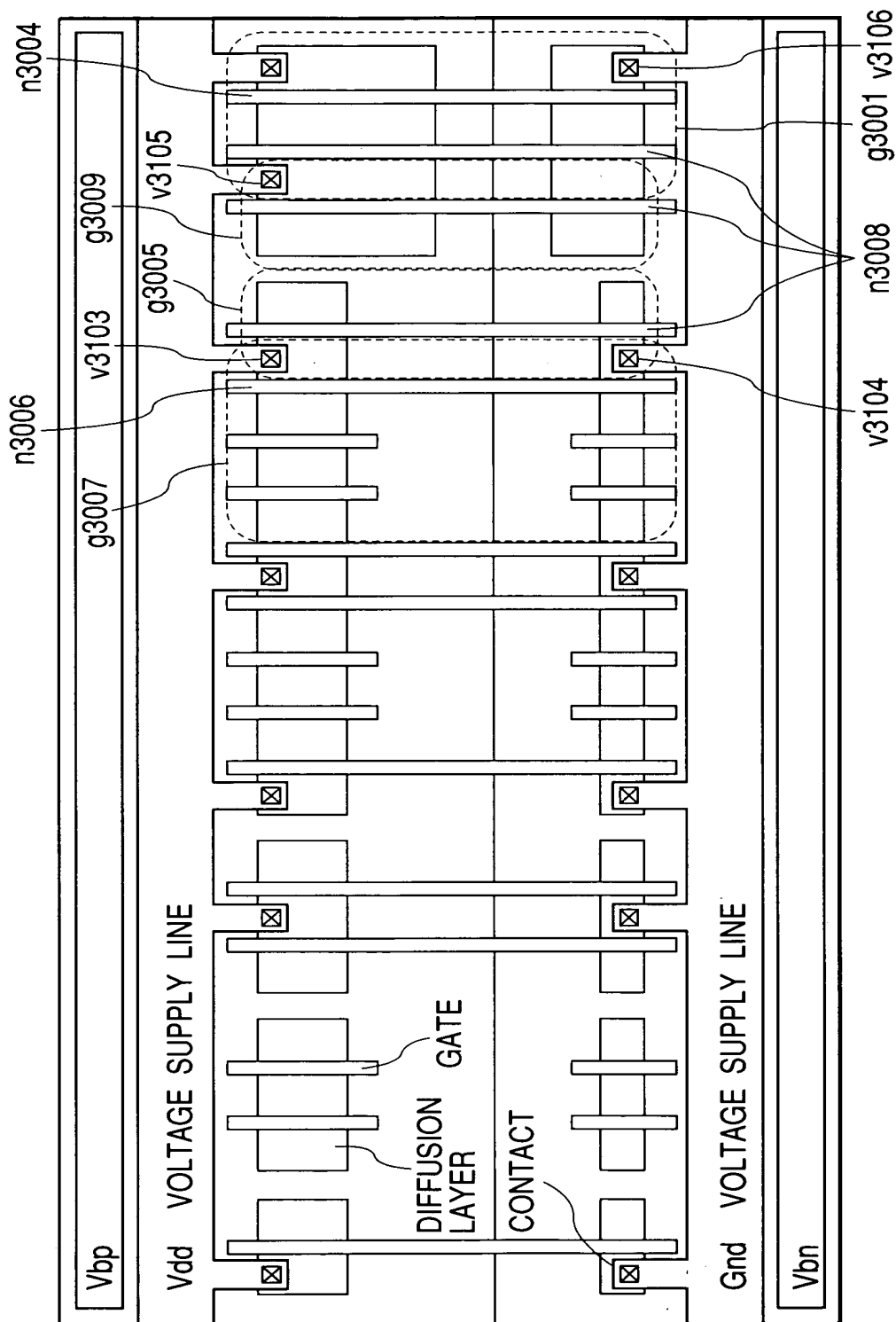






FIG. 21



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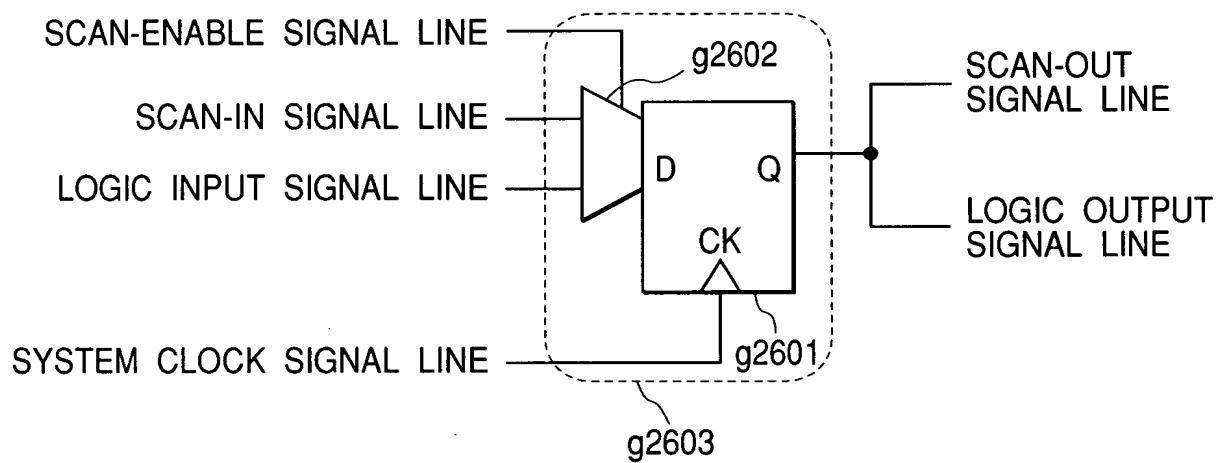
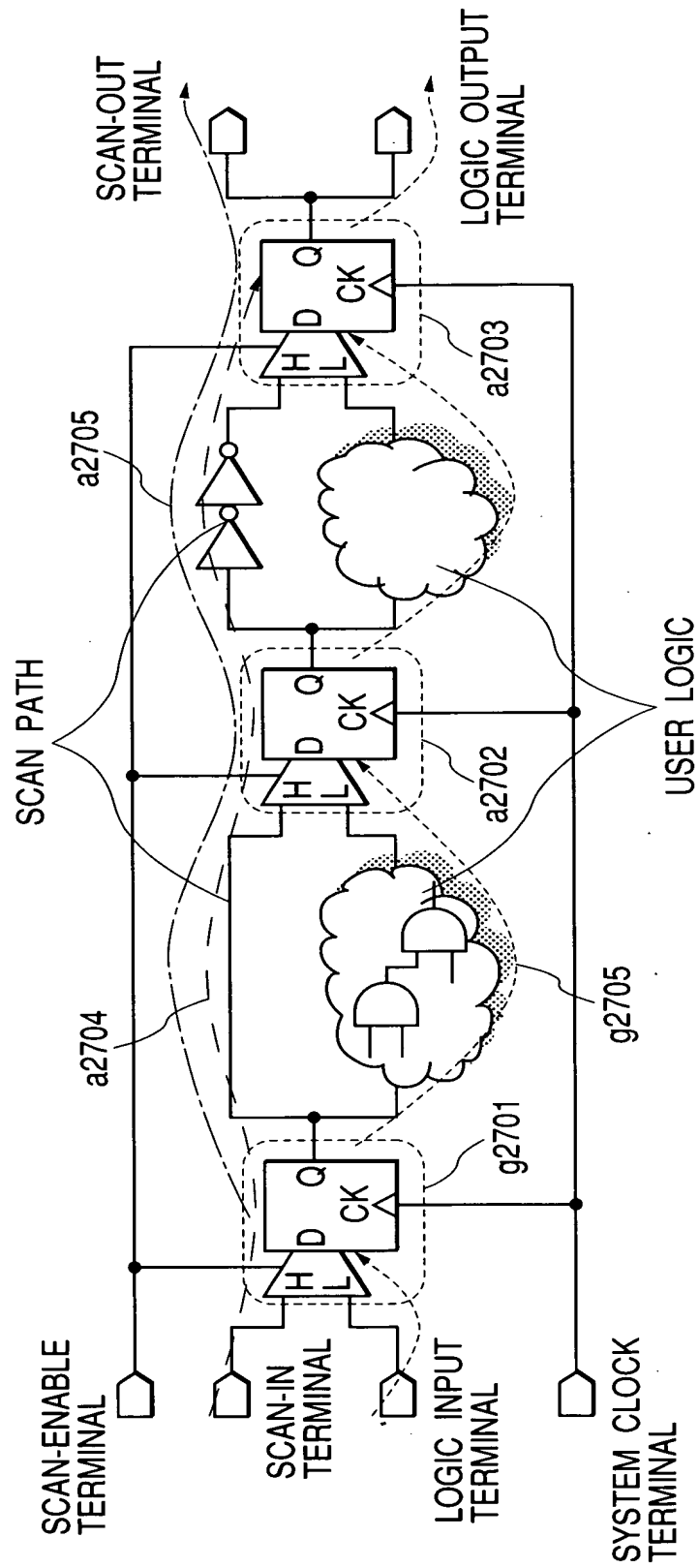


FIG. 23



*FIG. 24*